

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants	: Taylor et al.) I hereby certify that this paper is
) being filed electronically with the
U.S. Serial No.	: 10/829,495) U.S. Patent and Trademark Office
) on this date:
Filed	: April 22, 2004)
)
Title	: "Method and System for Fail-)
	Safe Renaming of Logical)
	Circuit Identifiers for) May 26, 2009
	Rerouted Logical Circuits in a)
	Data Network ")
)
)
) <u>/Felipe Hernandez/</u>
Art Unit	: 2419) Felipe Hernandez
) Registration No. 61,971
Examiner	: Ashley L. Shivers) Attorney for Applicants

RESPONSE TO THE FINAL OFFICE ACTION DATED MARCH 23, 2009

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the final action dated March 23, 2009, please consider the following remarks.

A status of the claims is reflected in the listing of the claims beginning on page 2 of this paper.

Remarks begin on page 8 of this paper.